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EC6612 VLSI DESIGN (VLSI) Lab Manual - StudentsFocus EC6612 VLSI DESIGN (VLSI) Lab Manual Anna University Regulation 2013 Electronic Communications Engineering (ECE) EC6612 VLSI DESIGN (VLSI) LAB Manual for all experiments is provided below. DSP Lab Manual - Rutgers University The DSP lab has both a software and a hardware component. In the software component, students carry ... Attendance is required in all hardware-lab sessions (see the lab schedule at the beginning of this manual.) It is not possible to receive a grade of "A" if one of these sessions is missed. Due to the limited number. DEPARTMENT OF ELECTRICAL - University of Central Florida LABORATORY MANUAL DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING UNIVERSITY OF CENTRAL FLORIDA EEE 3342 Digital Systems Revised August 2012 . CONTENTS Safety Rules and Operating Procedures Introduction Experiment #1 XILINX FPGA Tools Experiment #2 Simple Combinational Logic Experiment #3 Multi-Function Gate Experiment #4 Three-Bit Binary Added.

University - Laboratory Exercises The AI laboratory exercises cover the use of the Intel FPGA OpenCL SDK for accelerating artificial intelligence. The labs introduce topics in computer vision, autonomous driving, and machine learning.